

Access DB# 18710**SEARCH REQUEST FORM****Scientific and Technical Information Center**

Requester's Full Name: Pinchus Laufer
Art Unit: 2100 Phone Number 272-3599
Mail Box Location: 1C81

Examiner #: 73139 Date: 3/08/06
Serial Number: 10/667,241
Results Format Preferred (circle): PAPER DISK E-MAIL

If more than one search is submitted, please prioritize searches in order of need.

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: _____

Inventors (please provide full names): _____

Earliest Priority Filing Date: _____

**For Sequence Searches Only* Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.*

Litigation
5,961,614

Inventor : Kevin M. Christiansen

O.G. Date: June 22, 2004

STAFF USE ONLY

Searcher: Shirelle Green

Searcher Phone #: 571-272-3487

Searcher Location: 4B28

Date Searcher Picked Up: 3/8/06

Date Completed: 3/8/06

Searcher Prep & Review Time: _____

Clerical Prep Time: _____

Online Time: 15

Type of Search

Sequence (#) _____

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Litigation ☒

Fulltext _____

Patent Family _____

Other _____

Vendors and cost where applicable

STN

Dialog _____

Questel/Orbit

Dr.Link _____

Lexis/Nexis

Sequence Systems _____

WWW/Internet

Other (specify)

9.57

Courtlink 20

1 of 1 DOCUMENT

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

5961614

Link to Claims Section

October 5, 1999

System for data transfer through an I/O device using a memory access controller which receives and stores indication of a data status signal

REISSUE: Reissue Application filed Oct. 4, 2001 (O.G. Dec. 18, 2001) Ex. Gp.: 2782; Re. S.N. 09/972,847, (O.G. December 18, 2001)

October 4, 2001 - Reissue Application filed Ex. Gp.: 2182; Re. S.N. 09/972,847 (O.G. May 21, 2002)

September 18, 2003 - Reissue Application filed Ex. Gp.: 2182; Re. S.N. 10/667,241 (O.G. December 9, 2003)

September 18, 2003 - Reissue Application filed Ex. Gp.: 2182; Re. S.N. 10/667,241 (O.G. June 22, 2004)

INVENTOR: Christiansen, Kevin M. - Saratoga, California, United States (US)

APPL-NO: 647451 (08)

FILED-DATE: May 7, 1996

GRANTED-DATE: October 5, 1999

ASSIGNEE-AT-ISSUE: Apple Computer, Inc., Cupertino, California, United States (US), 02

ASSIGNEE-AFTER-ISSUE: August 8, 1996 - ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS)., APPLE COMPUTER, INC. ONE INFINITE LOOP CUPERTINO CALIFORNIA 95014, Reel and Frame Number: 08106/0975

ENGLISH-ABST:

A method and system for transferring units of data between a computer memory and an external system in which a DMA controller stores and uses information from an I/O device interfacing with the external system to transfer data more efficiently.

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Library: PATENTS
File: ALL

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- Remove some search terms.
- Use more common search terms, such as those listed in "Suggested Words and Concepts"
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File: JNLS

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LEXIS-NEXIS
Library: PATENTS
File: CURNEWS

prt ss 2 nonstop legalall

1/1 PLUSPAT - (C) QUESTEL-ORBIT- image
PN - US5961614 A 19991005 [US5961614]
STG - (A) United States patent
TI - (A) System for data transfer through an I/O device using a memory
access controller which receives and stores indication of a data
status signal
PA - (A) APPLE COMPUTER (US)
PA0 - Apple Computer, Inc., Cupertino CA [US]
IN - (A) CHRISTIANSEN KEVIN M (US)
IC - (A) G06F-013/28
AP - US64745196 19960507 [1996US-0647451]
PR - US43696995 19950508 [1995US-0436969]
- US64745196 19960507 [1996US-0647451]
EC - G06F-013/38A4
PCL - ORIGINAL (O) : 710022000; CROSS-REFERENCE (X) : 710017000 710019000
DT - Basic

1/1 LGST - (C) EPO
PN - US5961614 A 19991005 [US5961614]
AP - US64745196 19960507 [1996US-0647451]
ACT - 19960808 US/AS02-A
ASSIGNMENT OF ASSIGNOR'S INTEREST
OWNER: APPLE COMPUTER, INC. ONE INFINITE LOOP CUPERTINO,; EFFECTIVE
DATE: 19960729
- 19960808 US/AS02-A
ASSIGNMENT OF ASSIGNOR'S INTEREST
OWNER: CHRISTIANSEN, KEVIN M.; EFFECTIVE DATE: 19960729
- 20010807 US/CC-A
CERTIFICATE OF CORRECTION
- 20011218 US/RF-A
REISSUE APPLICATION FILED
EFFECTIVE DATE: 20011004
- 20031209 US/RF-A
REISSUE APPLICATION FILED
EFFECTIVE DATE: 20030918
- 20040622 US/RF-A
REISSUE APPLICATION FILED
EFFECTIVE DATE: 20030918
UP - 2004-26

1/1 CRXX - (C) CLAIMS/RRX
PN - 5,961,614 A 19991005 [US5961614]
PA - Apple Computer Inc
ACT - 20011004 REISSUE REQUESTED
ISSUE DATE OF O.G.: 20011218
REISSUE REQUEST NUMBER: 09/972847
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2782

Reissue Patent Number:

- 20011004 REISSUE REQUESTED
ISSUE DATE OF O.G.: 20020521
REISSUE REQUEST NUMBER: 09/972847
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2182

Reissue Patent Number:

- 20030918 REISSUE REQUESTED
ISSUE DATE OF O.G.: 20031209

REISSUE REQUEST NUMBER: 10/667241
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2182

Reissue Patent Number:

- 20030918 REISSUE REQUESTED
ISSUE DATE OF O.G.: 20040622
REISSUE REQUEST NUMBER: 10/667241
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2182

Reissue Patent Number:

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